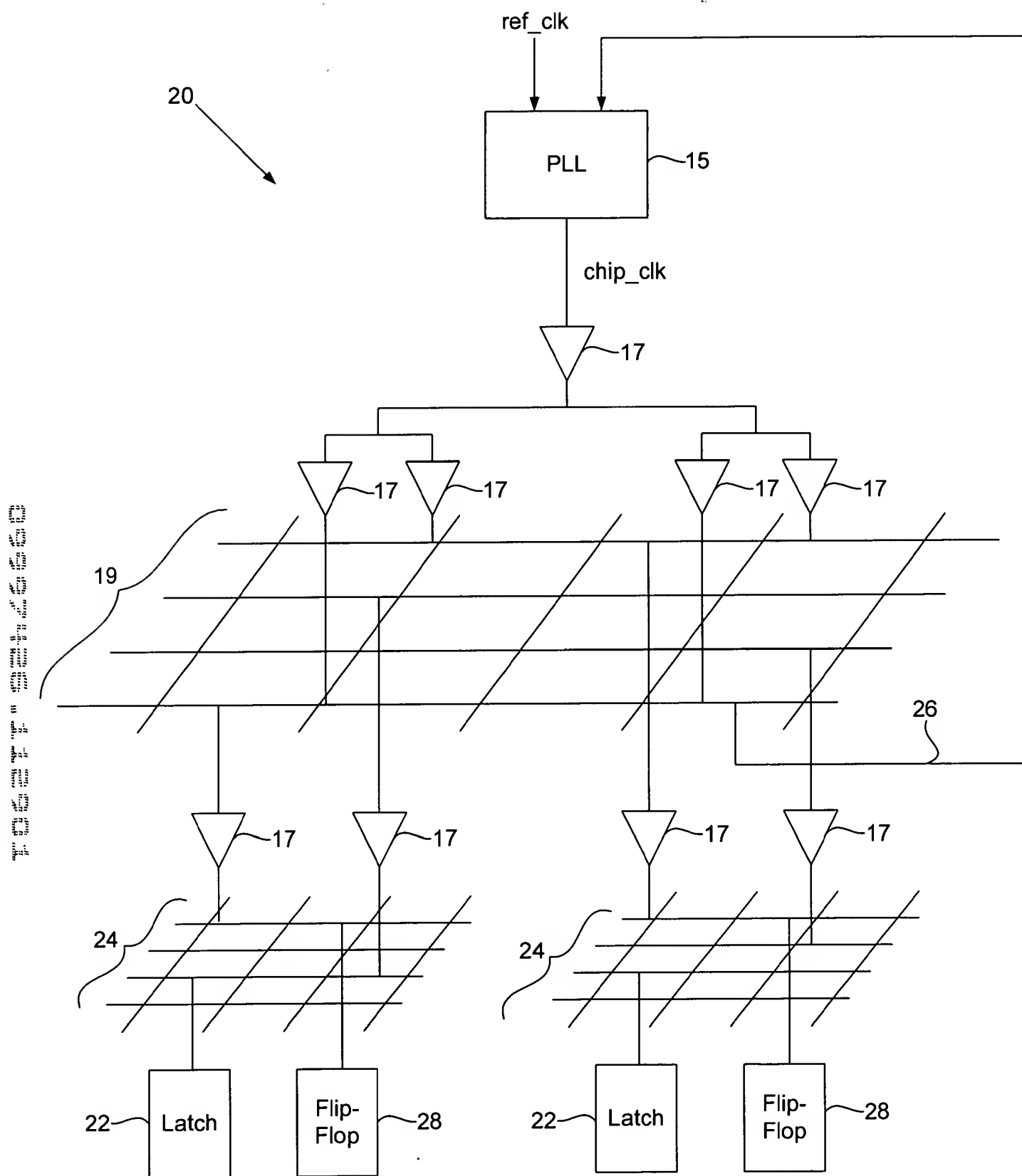
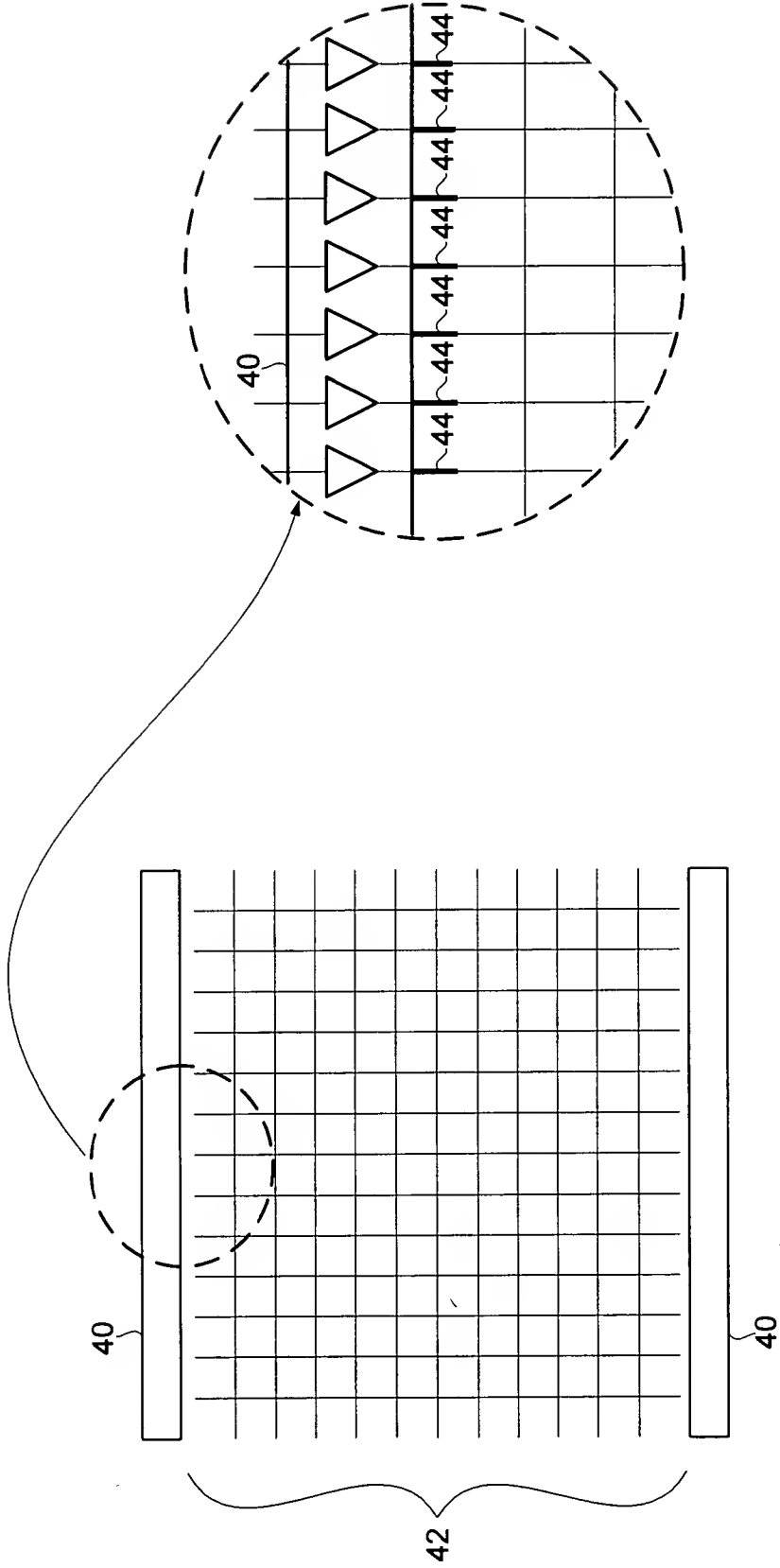


**FIGURE 1**  
**(Prior Art)**



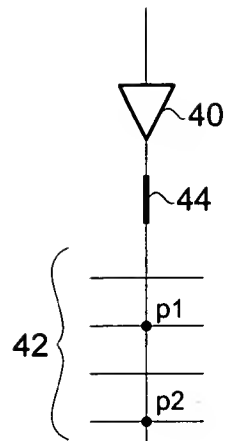
**FIGURE 2**  
(Prior Art)

FIG. 3a is a schematic diagram of a display panel 40. The display panel 40 includes a grid of pixels 42. A dashed circle indicates a magnified view of a portion of the grid, shown in FIG. 3b.

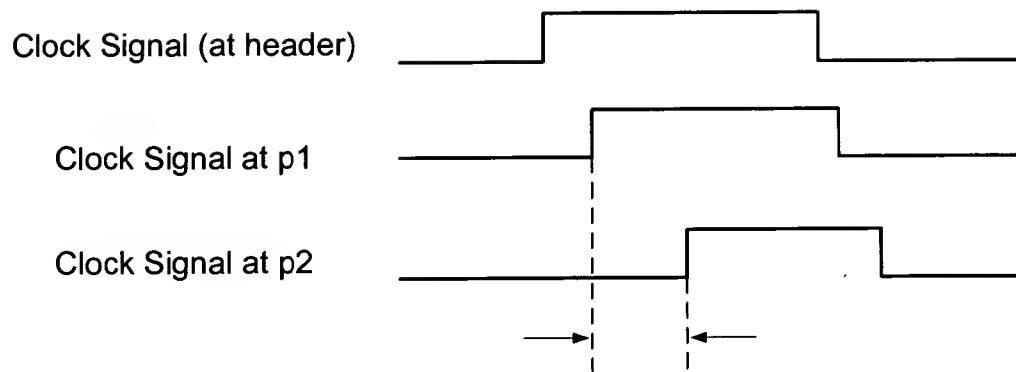


**FIGURE 3b**  
(Prior Art)

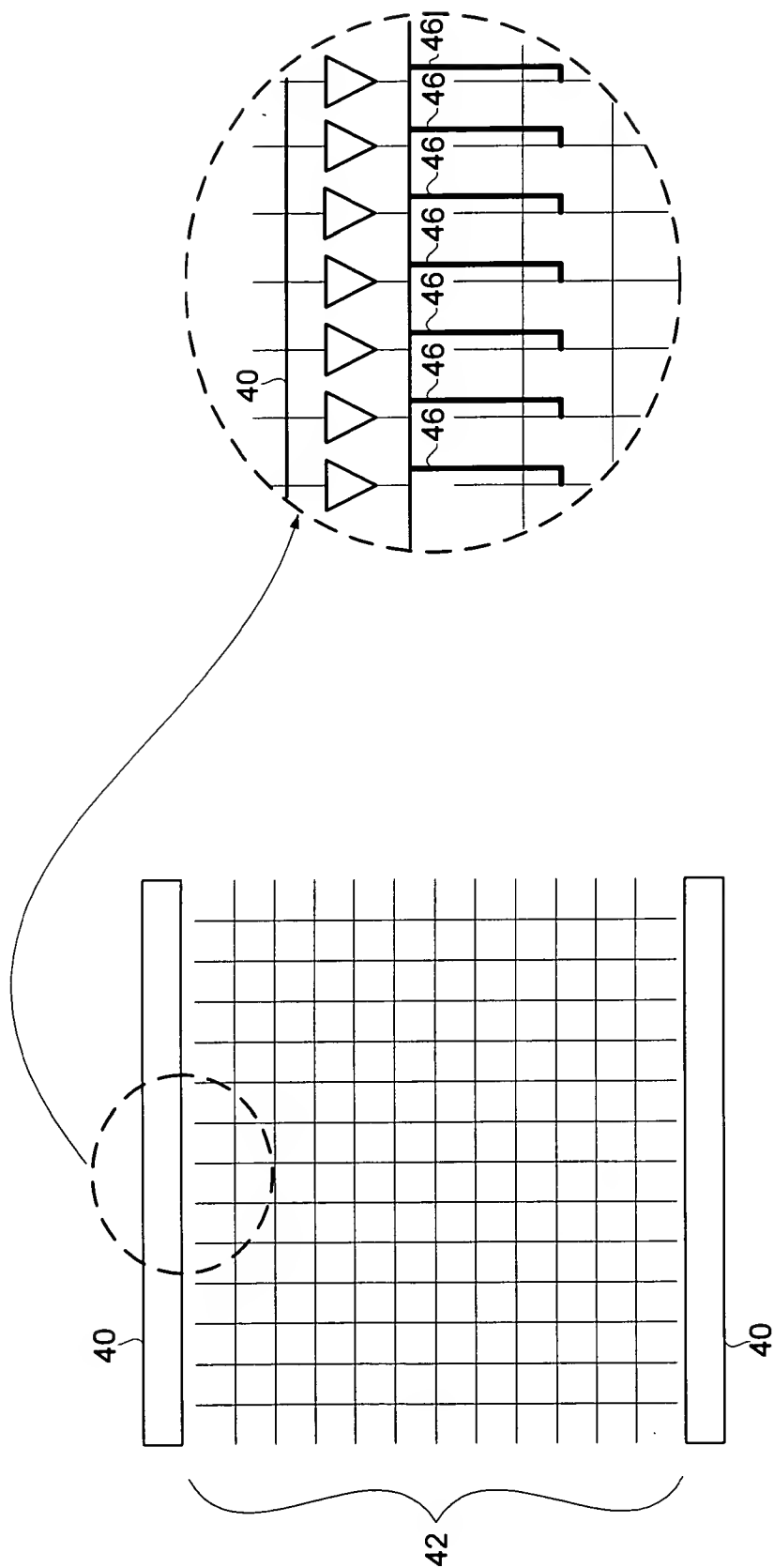
**FIGURE 3a**  
(Prior Art)



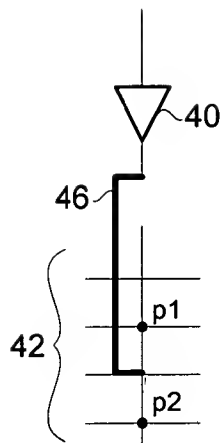
**FIGURE 3c**  
**(Prior Art)**



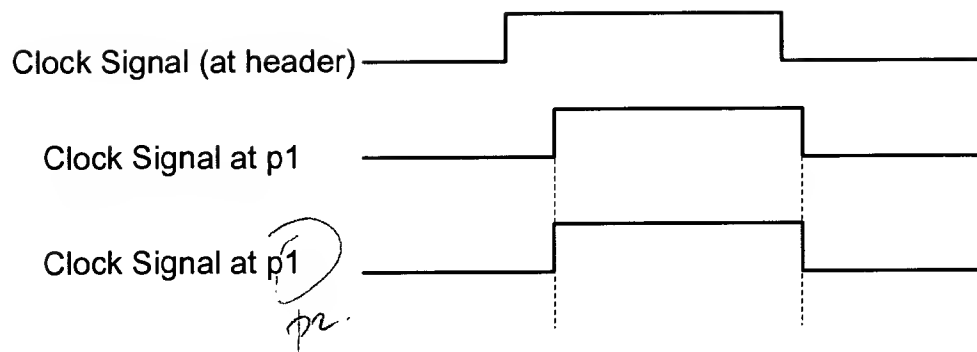
**FIGURE 3d**  
**(Prior Art)**



**FIGURE 4a**

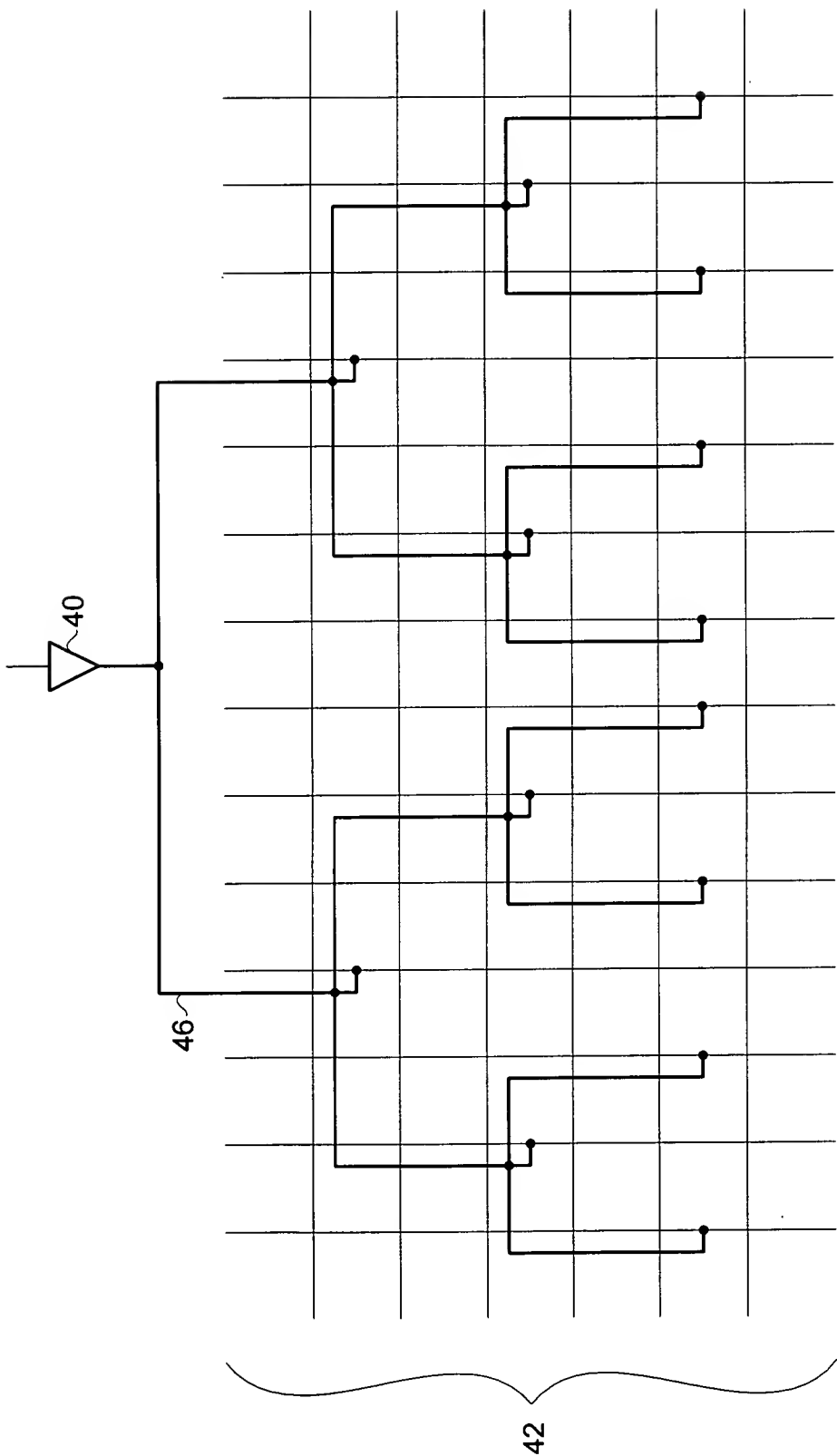


**FIGURE 4b**



**FIGURE 4c**

FIG. 5 is a schematic diagram of a circuit 40, which includes a plurality of input lines 42, a plurality of output lines 44, and a plurality of control lines 46. The circuit 40 is configured to receive a plurality of input signals and produce a plurality of output signals based on the received input signals and the control signals.



**FIGURE 5**